Simulation and analysis of GaN-based light-emitting diodes with diamond shaped

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ABSTRACT

Improving light extraction efficiency (LEE) is important to enhance efficiency of light-emitting diodes (LEDs) and the issue has been studied comprehensively. A GaN-based LED chip with diamond shaped is presented in this letter and LEE was analyzed. The LEEs of the conventional chip and flip chip with different angle of slant from 0° to 20° were obtained through Monte-Carlo ray tracing method, which is a useful and efficient way in studying the LEE of LED. The results show that the LEE of the chip with obliquity enhanced by 20.2% compared with conventional rectangle chip with mirror bottom surface, however, there is no improve of the LEEs of flip chip and conventional chip with diffuse surface.

Keywords: Diamond shaped, light-emitting diode, Monte-Carlo simulation, light extraction efficiency

1 INTRODUCTION

GaN-based light emitting diodes (GaN-LEDs) have been studied extensively for applications as next generation promising solid-state-lighting source^[1], however, the light efficiency is not high enough to satisfy people's requirement. Except for improving the internal quantum efficiency^[2], which has a relationship with dislocation, piezoelectric field, doping and so on^[3,4], to improve the light extract efficiency(LEE) of the GaN-LEDs is an important and efficient way to enhance the lumen efficiency . Many methods were proposed, such as pattern substrate, photonics crystal, surface roughed and other approaches^[5-8].

In this paper, we proposed a GaN-LED with diamond shape, which is different from conventional rectangle chip. The LEE of the chip of conventional chip and flip chip with different obliquity degree were simulated and analyzed using Monte-Carlo ray tracing method ^[9.10], which can simulate transmission through any material of photon and optical effects between the interfaces. Commercial GaN-LED chips now are rectangle shaped, no matter conventional chip or flip chip. For a diamond shaped chip, it can change the route like roughed surface with more simple techniques.

2 OPTICAL MODELING FOR GAN-LED

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Photonics and Optoelectronics Meetings (POEM) 2009: Solar Cells, Solid State Lighting, and Information Display Technologies, edited by Michael Grätzel, Hiroshi Amano, Chin Hsin Chen, Changqing Chen, Peng Wang, Proc. of SPIE Vol. 7518, 75181D · © 2009 SPIE · CCC code: 0277-786X/09/\$18 · doi: 10.1117/12.843471 Firstly, a conventional GaN-LED based on a simple model was analyzed to explain why it's LEE is low and made a brief exposition of our model, as shown in fig.1 (a) and fig.1 (b). The structure of the model consists of a substrate on the bottom, a thick n-type GaN layer, a thin InGaN/GaN active layer and a thick p-type GaN layer. The refractive index of each layer is shown in table 1 ^[11]. The critical angle of the light escape cone is about 23° in a conventional GaN-LED calculated from Snell's law : θ_c =arcsin(n₁/n₂), where n₁ and n₂ are reflective indexes of two different media,

respectively. The ratio of the photon generated from the active layer escape to the air from a certain surface is :

$$\eta_{\rm lee} = \frac{1}{4\pi} \int_0^{\theta c} 2\pi \sin(\theta) d\theta = \frac{1}{2} \left(1 - \frac{\sqrt{n_1^2 - n_2^2}}{n_1}\right) \tag{1}$$

It is only 4.2% to a GaN-LED if $n_1=n_{GaN}$ and $n_2=n_{air}$ was taken into the formula (1). The other photons are absorbed during they transmit in LED layer. When the route photon transmit in LED is changed because of roughed surface, the chance photon escape to the air enhances and LEE improves.

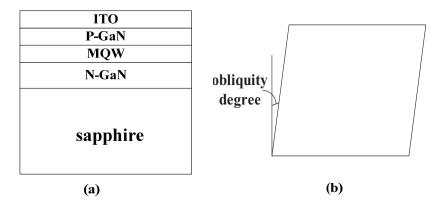


Fig.1 (a) the simple model of conventional chip structure. (b) the LED chip with a diamond shaped

The structure for the GaN-LED is based on the simple model and the parameters are shown in table 1. The chips of small size (300um*300um) and big size (1000um*1000um) were simulated. In the simulation, the effects of electric pad as well as current spreading were ignored for simplicity. Rays are generated randomly in the active layer and emit isotropically.

material	Sapphire	n-GaN	MQW	p-GaN	ITO
Thickness(um)	90	3	0.1	0.3	0.4
Refraction index	1.78	2.5	2.5	2.5	2

Table 1 Parameters of each layer in the simulated LED

3 RESULTS AND DISCUSSION

The LEEs of conventional wire bonding chip and flip chip with different oblique angle are shown in fig.2 and fig.3,

respectively. From fig.2 we can see that the conventional chip with diamond shaped can improve the LEE, especially for small chip with mirror reflection bottom surface, whose LEE enhanced by 20.2%. The simulation results show that the slanting surfaces can change the pathway of photon transmitting in GaN-LED chip. The LEEs improve

quickly when the obliquity angle of the chip increase from 0° to 5° and then LEEs improve slowly and became stable

for conventional chip with mirror reflection. However, if the bottom surfaces are diffuse reflection, the LEEs increase slightly. This is due to the roughed surface can change the route of most photons. In comparison with the diffuse reflecting surface, the slanting side surface is so small that only a few photons reflect at the surfaces, so there is no much improvement for the chip whose bottom surface is diffuse reflection. For the same reason, the LEEs are nearly a constant at different inclination angle for flip chip, which is shown in fig.3. The height of flip chip that have already removed substrate is only several micrometer, far lower than conventional chip that is why the LEE of flip chip is smaller than that of conventional chip under the condition of mirror surface.

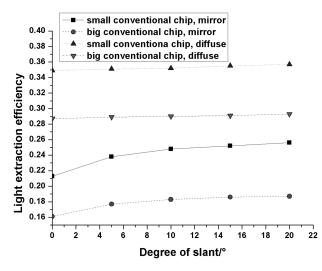


Fig.2 LEEs of conventional chip with different degree of slant.

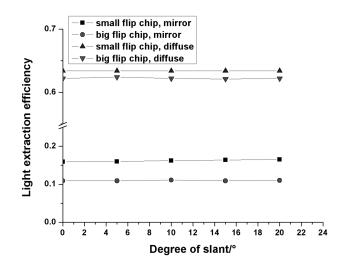


Fig.3 LEEs of flip chip with different degree of slant.

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Under the condition of conventional chip with mirror bottom surface and obliquity angle 5°, the LEEs were simulated under different thickness of the substrate, as shown in fig.4. The results revealed that LEEs increased with the increasing thickness of substrate and changed to be steady near 120um. Photons generated at the active layer (MQW) randomly and uniformly, only a small part of photons those are close to edge of chip change direction and escaped to the air, it can be proved from fig.5, which showed that the LEEs decrease with size increasing, however, the LEEs of rectangle shaped chip was higher nearly 3 percentage point than those of diamond shaped chip.

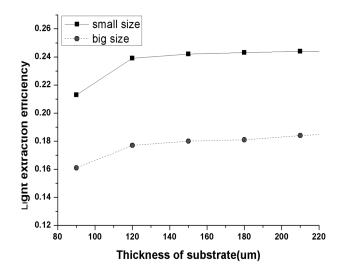


Fig.4 The LEEs of the conventional chip with different thickness of substrate

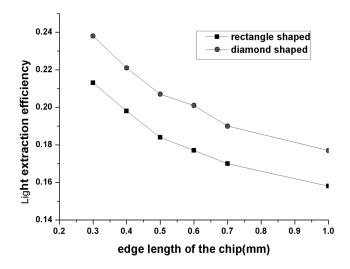


Fig.5 The LEEs of rectangle and diamond shaped conventional chip with different size

4 CONCLUSION

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In this paper, the LEEs of a GaN-LED chip with diamond shaped under different condition were simulated and analyzed based on Monte-Carlo ray trace method. In the simulation results, we conclude that the LEE of a small size chip with diamond shaped improved greatly. When size became bigger or any surface is diffuse reflection, the diamond shaped structures have no impact on the LEE. Anyway, diamond shaped chips have the simplest technique. The realistically properties of this kind of GaN-LED will be studied in our next work.

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