

Electrical Transport Mechanisms and Photovoltaic Characterisation of MgPc/p-Silicon Hybrid Organic–Inorganic Solar Cells

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Abstract: Hybrid organic–inorganic solar cell was fabricated by thin film of nanocrystalline magnesium phthalocyanine, MgPc, on p-type Si substrates using the conventional thermal evaporation technique. The measured electrical parameters were used to determine the predominant conduction mechanisms of these heterojunctions in the temperature range 298 to 373 K. The dark forward current was found to be increased exponentially with the applied voltage. Where the dark forward current density-voltage characteristics indicated a thermionic emission conduction over the MgPc/p-Si interface at relatively low voltages followed by a space-charge-limited current (SCLC) controlled by a single dominating trap level of 0.34 eV at relatively high voltages. On the other hand, the carrier generation-recombination process limits the reverse current. Also, electrical parameters were determined from the I–V characteristic. The capacitance–voltage characteristics indicated that the junction is of abrupt nature. Under illumination, the cell exhibits photovoltaic characteristics with an open-circuit voltage (V_{oc}) of 0.32 V, a short circuit current (I_{sc}) of 246 μ A, and a power conversion efficiency η of 2.71%. These parameters have been estimated at room temperature and under illumination of 6 mW/cm² white light.

1. INTRODUCTION

Metallophthalocyanines (MPcs) are well-studied macrocyclic compounds, which owing to their highly delocalised p-electron system exhibit, almost without exception, high thermal and chemical stability and wide range of intriguing physico-chemical properties, making the colorants useful for numerous applications [1,2]. The study of molecular semiconductors (MS), such as phthalocyanines (Pcs) has become in the last decade an active research and industrial field due to its interest for use in more advanced technological applications such as photovoltaic cells [3], laser printers [4], dyes, pigments, photocopying agents [5], gas sensors [6], optical data storage systems [7], and solar cells [8]. Perhaps one of the most interesting features, remaining a very active area of research, is the ability of MPc's to form complexes with electron donating molecules, which are additively bound to the central metal ion of MPc's. Heterojunction devices of p-MgPc/n-Si studied by Raid [9] in dark and under illumination showed that these junctions exhibit rectifying and strong photovoltaic characteristics with power conversion efficiency of 1.05%. Van *et al.* [10] concluded that the junction parameters, such as the rectification factor, RR, the saturation current density, J_s , and the ideality factor, n , are strongly influenced by the dopant, the thickness, and the preparation temperature of the polypyrrol layer for metallophthalocyanine doped pyrrole/silicon heterojunction.

The objective of this investigation is to study the dark and illuminated current–voltage (I–V) characteristics and the capacitance–voltage (C–V) characteristics of MgPc/p-Si heterojunction at different temperatures. The results obtained from these measurements were analyzed to determine some heterojunction parameters and to suggest the dominant current transport mechanism.

2. EXPERIMENTAL PROCEDURE

The MgPc powder used in this study is obtained from Kodak, UK. The p-type Si wafer with (100) orientation and hole concentra-

tion of 10^{22} m⁻³ samples is obtained from Nippon Mining Co. In order to remove the native oxide on surface on p-Si, the substrate was etched by CP4 solution (HF:HNO₃:CH₃COOH) in ratio (1:6:1) for 10s, after etching, the Si wafers were washed with distilled water and then with ethyl alcohol. The p-Si substrates were coated from one side by MgPc thin film of thickness 102nm using thermal evaporation technique to fabricate MgPc/p-Si heterojunction cell. The front contact of this heterojunction was made with gold mesh electrode to be used as ohmic electrode. The back contact was made by depositing a relatively thick film of Al to the bottom of the p-Si substrate. Thus, an Au/MgPc/p-Si/Al cell was obtained. A schematic diagram of the heterojunction device is given in Fig. (1). The fabricated MgPc/p-Si cell was annealed in air at 383K for 1h to complete the junction formation. Annealing of heterojunction is the usual step for obtaining the best efficiency cells. This annealing might remove any channels, which could be raised during the fabrication. The dark capacitance–voltage (C–V) characteristics for the fabricated cell were measured at 1MHz and at room temperature, using a computerised CV-410 meter (Solid State Measurement, Inc., Pittsburgh). The current density–voltage (I–V) characteristics of the fabricated cell were achieved by measuring the current corresponding to a certain potential difference across the junction, using a conventional circuit. The voltage across the junction and current passing through it were measured simultaneously using high impedance electrometers (Keithley 617). The dark I–V characteristics were obtained in a complete dark chamber at room temperature or inside a dark furnace in case of measurements at higher temperatures. Illumination characteristics for the same samples under 6 mW/cm² white light source (halogen lamp) were measured. The intensity of the incident light was measured using a digital lux meter (BCHA, model 93408).

3. RESULTS AND DISCUSSION

3.1. Dark Current–Voltage Characteristics

To study the junction properties, current–voltage characteristics (I–V) have been measured. These measurements, usually provide a valuable source of information about the junction properties, such as the rectification ratio (RR), the diode quality factor (m), the series resistance (R_s) and the shunt (R_{sh}) resistance. Analysis of the I–

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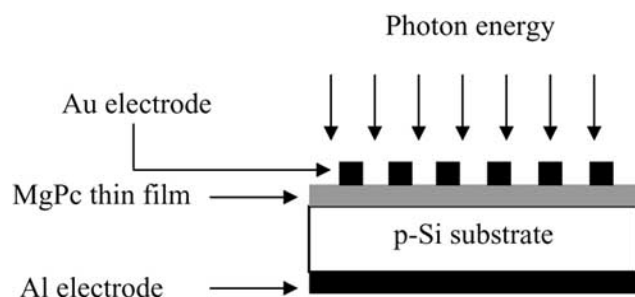


Fig. (1). Schematic diagram of Au/MgPc/p-Si/Al solar cell.

V characteristics is also extremely useful to identify the transport mechanisms controlling the conduction. The current–voltage characteristics of MgPc films with thickness 102 nm deposited onto p-Si at different temperatures ranging from 298 to 373 K is illustrated in Fig. (2). The device clearly exhibits the rectification behaviour in dark, which is enhanced by increasing the temperatures. The curves exhibited diode behaviour, with the forward direction to the positive potential on substrate. This behaviour can be understood by the formation of p-p⁺ heterojunction, namely the barrier at the interface, limits forward and reverse carrier flow across the junction, where the built-in potential could be developed.

The ratio of the forward current to the reverse current at a certain applied voltage is defined as the rectification factor RR. It is evident that the junction exhibits strong rectifying characteristics showing diode-like behaviour of RR was estimated as $\approx 10^2$ for Au/MgPc/p-Si heterojunction at ± 1 V. The series and shunt resistances (R_s and R_{sh}), which are important factors in improving cell performance and design, obtained at room temperature by the method stated in [11]. Their values can be calculated from the slope of the linear part of the forward and reverse I-V curve at room temperature, respectively. The obtained values of the series resistance (R_s) and the shunt resistance (R_{sh}) at room temperature were determined to be $376 \, \Omega$ and $43 \times 10^3 \, \Omega$, respectively.

The information about the conduction mechanisms can be obtained from current–voltage characteristics at different temperatures. Semi-logarithmic plots of the forward current–voltage for an Au/MgPc/p-Si heterojunction in the temperature range of 298–373

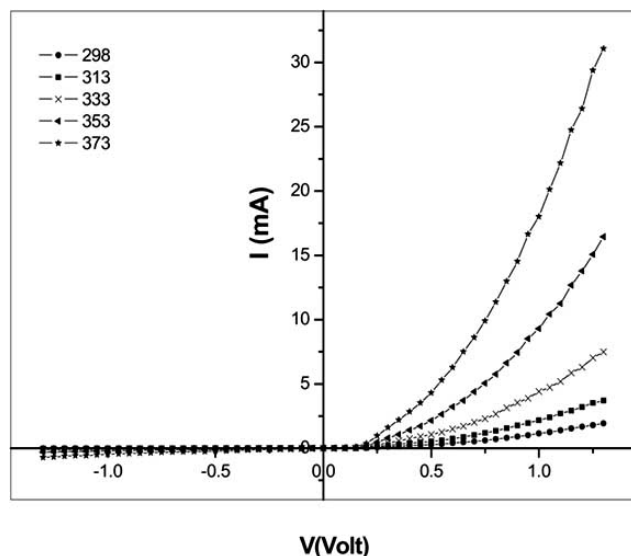


Fig. (2). I-V characteristics of MgPc/p-Si heterojunction at different temperatures in forward and reverse bias.

K are given in Fig. (3). Two distinct regions characterise these curves indicating different conduction mechanisms. As observed within the narrow low forward voltage ($V \leq 0.25$ V), the current increases exponentially. Such behaviour agrees with rectification characteristics, which are generally described by either the diffusion model, the emission model, or the recombination model [12]. Thus, the voltage dependence of the junction current can be expressed in the simplified form as

$$I = I_o \exp\left(\frac{eV}{mk_B T}\right) \quad (1)$$

Where I_o is the saturation current, which can be obtained by extrapolation of the linear $\ln(I)$ -V portion to the $\ln(I)$ axis at zero voltage and m is the diode quality factor. The parameters I_o and m can be readily determined from the curve (room temperature measurement) shown in Fig. (3) together with Eq. (1). The values of I_o and m are calculated as 1.63×10^{-7} A and 1.62 ± 0.03 , respectively. Deviation of m from unity may be attributed to either recombination of electrons and holes in the depletion region, and/or the increase of the diffusion current due to increasing the applied voltage [13]. The diode quality factor is almost found to be constant at different temperatures. This behaviour is in accordance with the thermionic emission mechanism. To confirm that the thermionic emission is the operating conduction mechanism, more analysis was carried out. According to the thermionic conduction, the saturation current is given by [14]

$$I_o \propto T^2 \exp\left(-\frac{e\phi_b}{k_B T}\right) \quad (2)$$

Where ϕ_b is the barrier height. The obtained straight line from the plot of logarithm of I_o/T^2 against $1/T$ supports the thermionic mechanism. Fig. (4) represents this plot, which clearly shows a linear behaviour. From the slope ($-e\phi_b/k_B$) of the straight line the potential barrier height, ϕ_b , is determined as 0.48 eV.

In the second region (higher voltage), other conduction mechanism seems to be predominant. It is observed that the current shows a power dependence of voltage with slope of $\log J$ - $\log V$ of about 2 as seen from plot in Fig. (5). This power dependence suggests that

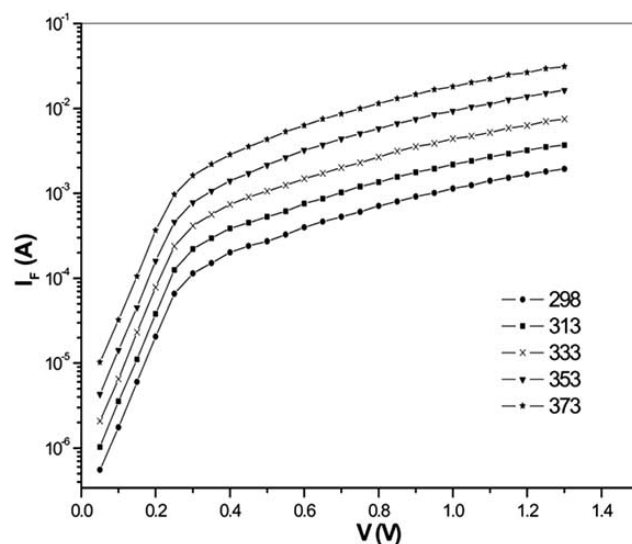


Fig. (3). Semilogarithmic plots of the forward bias of I-V characteristics at different temperature.

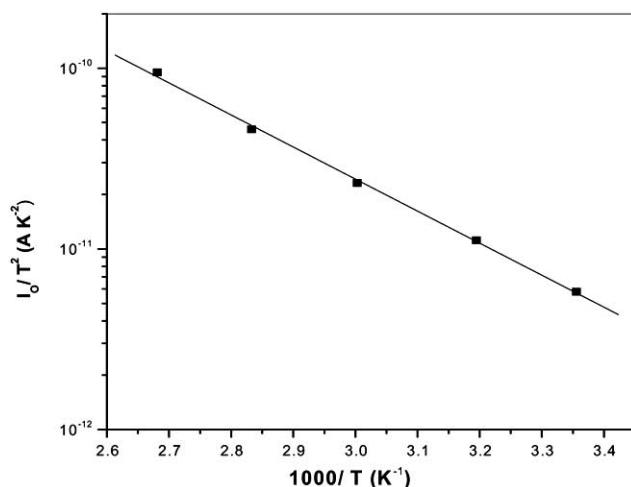


Fig. (4). Semilogarithmic plots of $\ln(I_0/T^2)$ versus $1/T$ for MgPc/p-Si heterojunction.

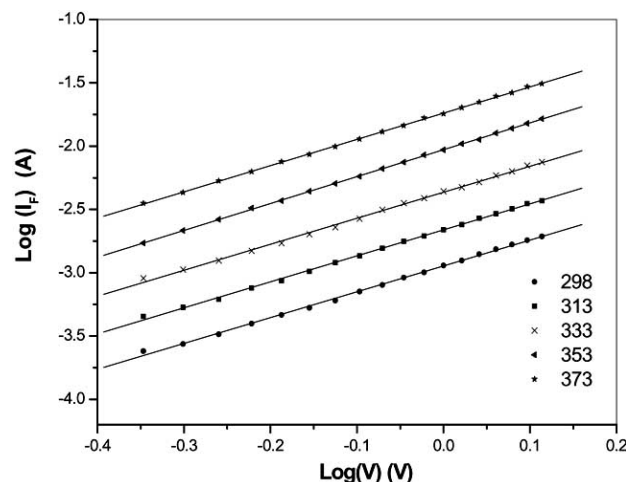


Fig. (5). Variation of $\log I$ with $\log V$ at higher forward voltage bias for MgPc/p-Si heterojunction.

the dark current is a space-charge-limited current (SCLC) dominated by a single trap level. According to Lampert theory, the relation for the current density in this case is given by [15]

$$I = (9/8)\epsilon\mu\theta \frac{V^2}{d^3}, \quad (3)$$

Where d is the thickness of the MgPc film, ϵ_2 is the permittivity of MgPc, μ is the mobility of holes, and θ is the trapping factor, which is defined by the ratio of the free charge to trapped charge and given by

$$\theta = \frac{N_v}{N_{t(s)}} \exp\left(\frac{-E_t}{k_B T}\right) \quad (4)$$

Where k_B is Boltzmann's constant, N_v is the effective density of states in the valence band edge, and $N_{t(s)}$ is the total trap concentration at energy level E_t above the valence band edge.

The value of trap energy level E_t can be calculated by plotting $\ln I$ versus $1000/T$ as shown in Fig. (6) from the slope of the straight line E_t is determined as 0.34 eV were obtained assuming N_v

$= 10^{21} \text{ cm}^{-3}$ and using $\mu = 4.15 \times 10^{-2} \text{ cm}^2 \text{ V}^{-1} \text{ s}^{-1}$, which is measured by us in different work under publication.

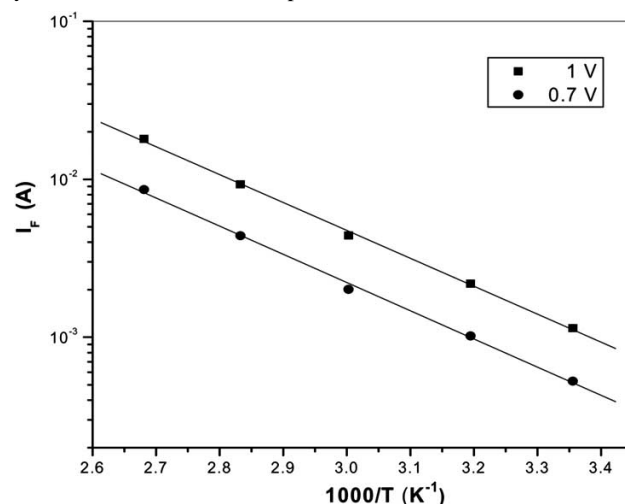


Fig. (6). Variation of $\log I_F$ with $1000/T$ in SCLC for MgPc/p-Si heterojunction.

On the other hand, the reverse current–voltage characteristics of MgPc/p-Si heterojunction at different temperatures ranging from 298 to 373 K are shown in Fig. (7). As usual for reverse direction of a rectifier, the current does not nearly depend on voltage. At relatively high voltage, the dependence of the current on voltage is stronger than predicted by pure thermionic emission or Schottky effect that leads to the assumption that nearly flat reverse I – V characteristics could be fit over a wide range of voltages assuming that generation and recombination of carriers in the Si substrate is the dominant source of the reverse current [4].

The reverse current due to carrier recombination is thermally activated according to the form [16]

$$I_R \propto \exp(-\Delta E / k_B T) \quad (5)$$

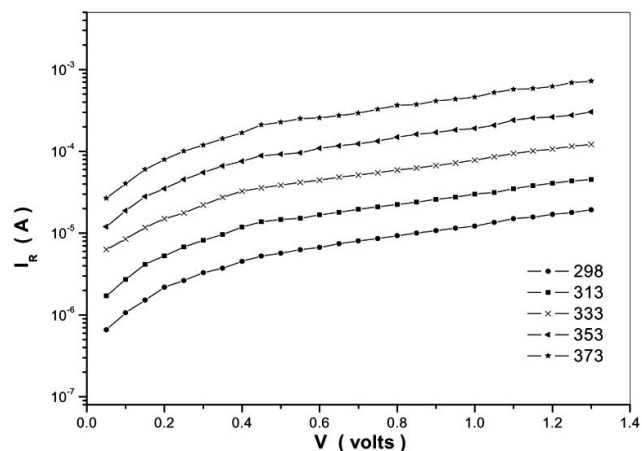
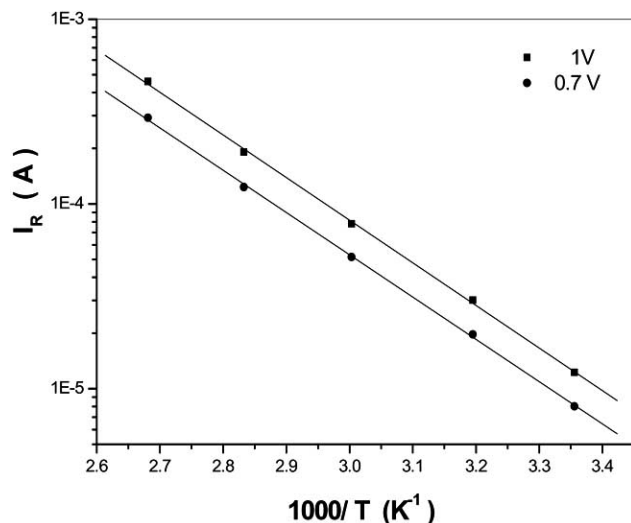
Where ΔE is the carrier activation energy. The dependence of the reverse current I_R on $1/T$ is shown in Fig. (8). As it is shown, the activation energy is determined from the slope of the straight line, which has a value of 0.51 eV. The obtained activation energy is approximately equal to half the band gap of Si; which suggests that the main source of the reverse current is Si substrate and indicates that the reverse current should be limited by the carrier generation process.

3.2. Dark Capacitance–voltage Characteristics

The capacitance of the MgPc/p-Si cell was measured at a high frequency of 1 MHz in dark and at room temperature as shown in Fig. (8). As observed from this figure, $1/C^2$ – V variation is linear in the voltage range studied indicating that the junction is of abrupt nature. For abrupt junction, the junction capacitance as a function of reverse-bias potential is given by [17]

$$\frac{1}{C^2} = -\frac{2}{eN\epsilon\epsilon_o}(V_{bi} - V) \quad (6)$$

By plotting $1/C^2$ versus V , a straight line is obtained, the slope gives the free carrier concentration, N , of the semiconductor and the intercept at $1/C^2 = 0$ gives the built-in-voltage, V_{bi} .

Fig. (7). Semi logarithmic plots of I_R against for MgPc/p-Si hetero junction.Fig. (8). Semilogarithmic plots of I_R versus $1/T$ for MgPc/p-Si hetero junction at different voltage.

The free carrier concentration, N , was found to be $\sim 2.69 \times 10^{17} \text{ cm}^{-3}$ and the built-in-voltage V_{bi} was estimated to be 0.32V. The value V_{bi} obtained from C-V measurements is in agreement with the value of the potential height, ϕ_b , that obtained from the I-V measurements.

The width of the depletion region width, W , is related to the junction capacitance at $V = 0$ V, C_o by the relation [9, 18]

$$W = \epsilon \epsilon_o A / C_o \quad (7)$$

The capacitance of the device, C_o , at zero bias was measured and found to be ~ 1.3 nF.

The maximum electric field is given by [19]

$$E_{\max} = 2V_{bi}/W \quad (8)$$

The values of W and E_{\max} are estimated to be $0.34 \mu\text{m}$ and $1.88 \text{ V}/\mu\text{m}$.

3.3. Photovoltaic Properties

The current-voltage characteristics of a MgPc/p-Si hetero junction solar cell in the dark and under white light illumination of 6 mWcm^{-2} and active area of 0.25 cm^2 are shown in Fig. (10). It can be seen from this figure that the current value at a given voltage for MgPc/p-Si cell under illumination is higher than that in the dark.

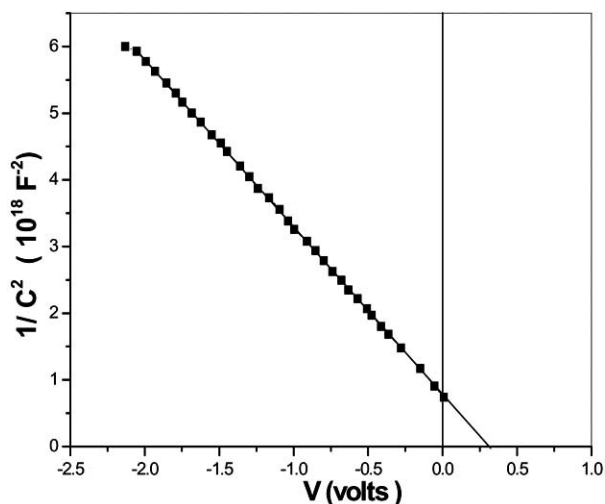
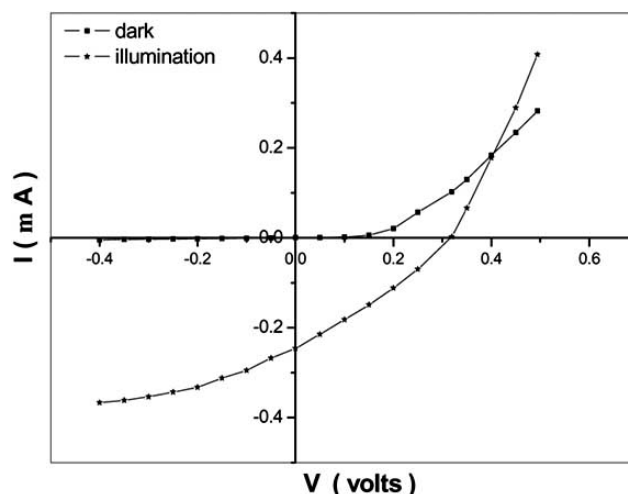
Fig. (9). C^2 -V characteristics for MgPc/p-Si hetero junction.

Fig. (10). Current-voltage characteristics under illumination for MgPc/p-Si hetero junction.

This indicates that the light generates carrier-contributing photocurrent due to the production of electron-hole pairs as a result of the light absorption. It is well known that the absorption region of all phthalocyanines corresponds to the UV-VIS region [19]. It has been suggested that the light is absorbed at both MgPc and Si. The generation of photoelectrons, via both MgPc and Si exciton intermediate, is followed by electron transfer from p-Si into MgPc through the potential barrier at the interface. This is a result of a difference in electron affinities between the two semiconductors. It is note worthy that the photocurrent in the reverse direction is strongly enhanced by photo-illumination. This behaviour yields useful information on the electron-hole pairs, which were effectively generated in the junction by incident photons. Under the influence of the electric field at the junction, electrons were accelerated towards the MgPc, while the generated holes were swept towards the Si along the potential barrier at the interface. Different junctions were fabricated with different thicknesses of MgPc layers. This can be explained as the increasing of the thickness increases the series resistance of the junction. It can also be explained by considering that the thinner layer allows higher intensity of light to reach the Si substrate.

The incident light produced a short circuit current, I_{sc} , and an open-circuit voltage, V_{oc} . The solar cell parameters are obtained from the following equations [20], where:

The experimental efficiency of the solar cell is given by

$$\eta = \frac{P_M}{P_{in}} = FF \frac{J_{sc} V_{oc}}{P_{in}} \times 100 \quad (9)$$

Where P_{in} is illumination power impinging on the cell that was previously estimated by 6 mW/cm^2 and J_{sc} ($= I_{sc}/A$) is short circuit and FF is the filling factor given by

$$FF = V_M I_M / V_{oc} I_{sc} \quad (10)$$

Where V_M and I_M are potential and current-densities at maximum power point, therefore, the maximum power obtained from the cell is given by

$$P_M = V_M I_M = FF V_{oc} I_{sc} \quad (11)$$

The solar cell parameters of fabricated cells have been calculated and given as short circuit current (I_{sc}) of $246 \mu\text{A}$, open circuit voltage (V_{oc}) of 0.32 V , fill factor (FF) of 0.31 , and power conversion efficiency (η) of $2.71\% \pm 0.03\%$. The value of the fill factor is lower in comparison to those of solar cells based on inorganic materials. The main cause of this effect is generally found to be the field dependent nature of the charge photogeneration process or high series resistance of the organic layer. However, the obtained value of η is in the order of the values, previously, reported for phthalocyanines heterojunction cells having Si as substrates [9].

4. CONCLUSIONS

The MgPc films were deposited onto p-Si <100> substrates by thermal evaporation technique. The dark current–voltage measurements suggest that the forward current transport in these junctions involve thermionic emission of the electrons from p-Si over the MgPc/Si barrier at low forward bias. Moreover, a space-charge-limited transport across the MgPc layer dominates due to the exponential distribution of traps above the valence band in the band gap of the MgPc layer at high forward bias. From the capacitance–voltage, measurements at high frequency (1 MHz) information can be obtained about the depletion layer extending in the Si side. These characteristics are interpreted by assuming the abrupt hetero-

junction model. Under illumination of $\sim 6 \text{ mW/cm}^2$, the significantly high $V_{oc} = 0.32 \text{ V}$ for the MgPc/p-Si junction, as compared to that (0.2 V) for the Au/p-Si cell, suggests that the excitons play an important role in the primary process of photo-carrier generation in MgPc. The obtained power conversion efficiency of the studied cell was $2.71 \pm 0.03\%$, which is comparatively high compared to those of other organic solar cells.

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